

I. a. Construct the quotient algebra $\mathbf{Nat}/\equiv_{\text{Mod}n}$ corresponding to this congruence.

The carrier of this quotient algebra is the set of the equivalence classes $\{x_0 \mid x_0 = ns\}$, $\{x_1 \mid x_1 = ns+1\}$, \dots , $\{x_{n-1} \mid x_{n-1} = ns+(n-1)\}$ ($s \in \text{Nat}$). The operations on these classes are defined in a natural way:

zero = 0

one = 1

(in the tables below, take the result of the operations to be the part excluding the nX ; and 0 if there is none.)

+	ns	$ns+1$	$ns+2$...	$ns+n-1$
nr	$n(r+s)$	$n(r+s)+\mathbf{1}$	$n(r+s)+\mathbf{2}$...	$n(r+s)+(\mathbf{n-1})$
$nr+1$	$n(r+s)+\mathbf{1}$	$n(r+s)+\mathbf{2}$	$n(r+s)+\mathbf{3}$...	$n(r+s+1)$
$nr+2$	$n(r+s)+\mathbf{2}$	$n(r+s)+\mathbf{3}$	$n(r+s)+\mathbf{4}$...	$n(r+s+1)+\mathbf{1}$
...	
$nr+(n-1)$	$n(r+s)+(\mathbf{n-1})$	$n(r+s+1)$	$n(r+s+1)+\mathbf{1}$...	$n(r+s+1)+(\mathbf{n-2})$

\times	ns	$ns+1$	$ns+2$...	$ns+(n-1)$
nr	$n(nrs)$	$n(nrs+r)$	$n(nrs+2r)$...	$n(nrs+n-1r)$
$nr+1$	$n(nrs+s)$	$n(nrs+r+s)+\mathbf{1}$	$n(nrs+2r+s)+\mathbf{2}$...	$n(nrs+n-1r+s)+\mathbf{n-1}$
$nr+2$	$n(nrs+2s)$	$n(nrs+r+2s)+\mathbf{2}$	$n(nrs+2r+2s+1)$...	$n(nrs+n-1r+2s+1)+\mathbf{2}$
...	
$nr+(n-1)$	$n(nrs+(n-1)s)$	$n(nrs+r+(n-1)s)+(\mathbf{n-1})$	$n(nrs+2r+(n-1)s+1)+\mathbf{2}$...	$n(nrs+(n-1)(r+s)+n-2)+\mathbf{1}$

To be more concrete: if, for instance, $n = 4$, then the carrier of $\mathbf{Nat}/\equiv_{\text{Mod}4}$ is the set of the equivalence classes $\{x_1 \mid x_1 = 4s\}$, $\{x_2 \mid x_2 = 4s+1\}$, $\{x_3 \mid x_3 = 4s+2\}$, $\{x_4 \mid x_4 = 4s+3\}$ ($s \in \text{Nat}$).

b. Show that the quotient algebra $\mathbf{Nat}/\equiv_{\text{Mod}4}$ is isomorphic to the algebra **Mod4**.

(i) The relation between the members of the carrier $\text{Nat}/\equiv_{\text{Mod}4}$ and **Mod4** is a one-to-one onto mapping. (Each equivalence class mapping to a member of **Mod4**).

(ii) The operations of $\mathbf{Nat}/\equiv_{\text{Mod}4}$ are defined as illustrated in (a). The operation tables given above and those for **Mod4** are identical, once we leave out the nX parts out of consideration.